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\*\* CONTINUING DATA \*\*\*\*\*

*None.*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY ITALY	SHEETS DRAWING 4	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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## TITLE

Manufacturing process for a high voltage transistor integrated on a semiconductor substrate with non-volatile memory cells and corresponding transistor

FILING FEE

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 1072

 FEES: Authority has been given in Paper  
 No. \_\_\_\_\_ to charge/credit DEPOSIT ACCOUNT  
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